

ABSTRACT OF THE DISCLOSURE

An apparatus comprising an analog circuit, a first digital circuit, and a second digital circuit. The analog circuit may be configured to generate a plurality of samples of an input signal in response to a plurality of phases of a reference clock.

5 The first digital circuit may be configured to generate (i) one or more data signal, (ii) a first strobe signal, and (iii) a second strobe signal in response to the plurality of samples, the plurality of phases, and a correction signal. The second digital circuit may be configured to generate the correction signal and a width signal in response to (i) the one or more data signal, (ii) the first strobe signal, and (iii) the second strobe signal.